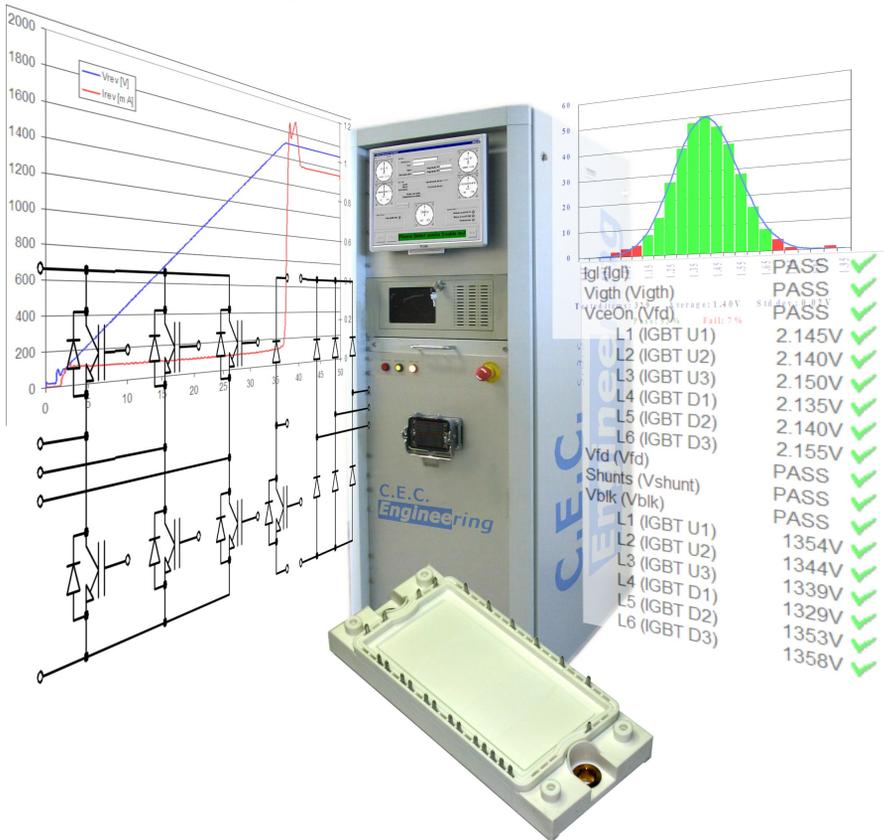


CEC Power Electronics Tester



Modular architecture for power semiconductors testing

- Discrete components or modules
- Reverse voltage up to 2500V @ 50mA
- Forward current up to 6000A @ 35V
- Gate parameters (iGBT, MOSFET, SCR...)

What is it

A modular architecture for power semiconductors testing, discrete or modules

Who can need it

components manufacturers end-of-line or intermediate production tests
 components user and assemblers class selection, incoming material quality check, final product test
 laboratories components characterization, stress tests

How to use it

Very easy to use, although complete and fully configurable; it requires no particular expertise, nor software writing.
 Error safe: it cannot be damaged by improper programming.

Integrability

It can work as a stand-alone equipment, or together with an external automation, either in master or slave mode

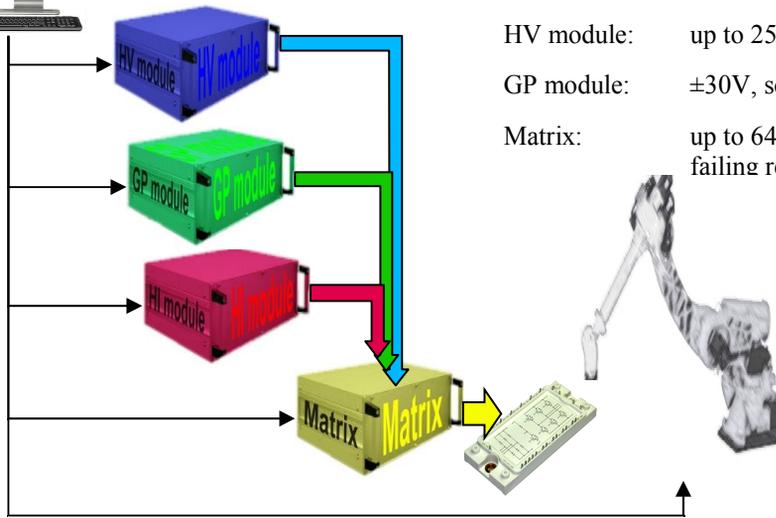
Modularity

It can be equipped with the minimum required features: it can be small and specific, or complete an general purpose.
 It can be configured to test discrete components or modules. It can contact DUT manually or connected to an automation.

It runs usual static tests like

Reverse voltage: blocking voltage, reverse leakage current, voltage class selection, surge test with reapplied reverse voltage
 Forward bias: forward voltage drop, temperature rise
 Gate parameters: trigger voltage and current (I_{gt}, V_{gt}), latch current, hold current, insulated gate leakage current, insulated gate trigger voltage.

Custom tests can be implemented on request.



Electrical characteristics

- HI module: 100A, 1kA, ... 6kA @ 35V
- HV module: up to 2500V, scales from 100µA to 50mA
- GP module: ±30V, scales from 100nA to 1A
- Matrix: up to 64*3 points (Power + Gate + Sense) failing relay search tool

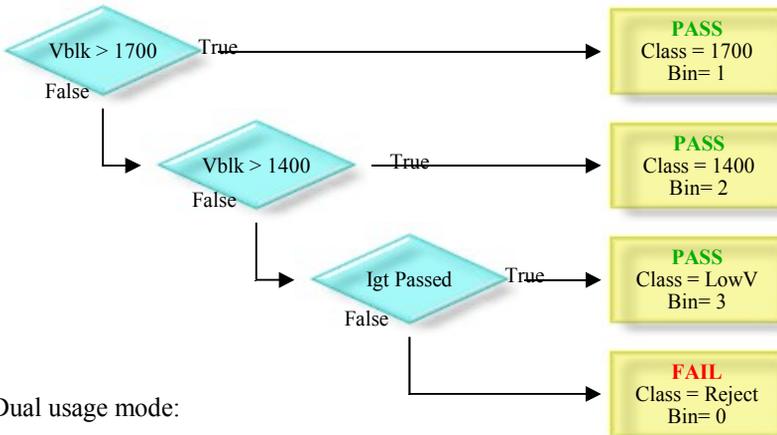
Synchronism between generation and measurement and between involved generators
 Generation and measurement resolution: 12bit minimum
 Ready for connection to contacting tools and automations.

TESTPROGRAM:
 fully configurable and easy to define (no software writing is required)

The steps sequence is defined specifying the kind of test, its specific parameters, which junctions it must run on, where to jump if Pass, Fail, or depending on result value ranges. Parameters can be function of other tests.

Different steps partial results can be used for class selection and binning.

It can loop on a test iterating some parameters (for characterization) or not (stress cycles).

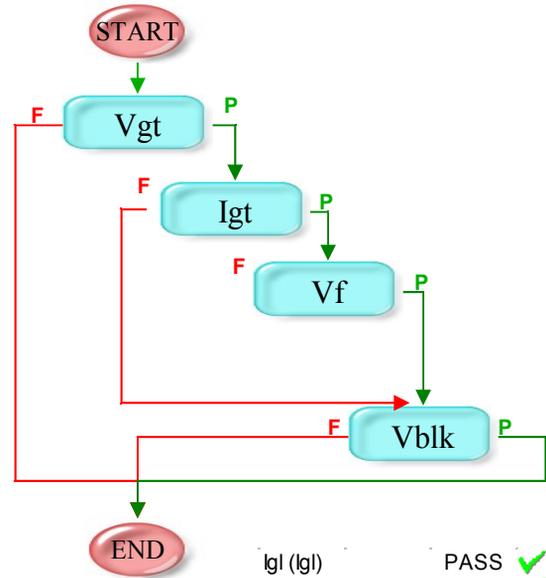


Dual usage mode:

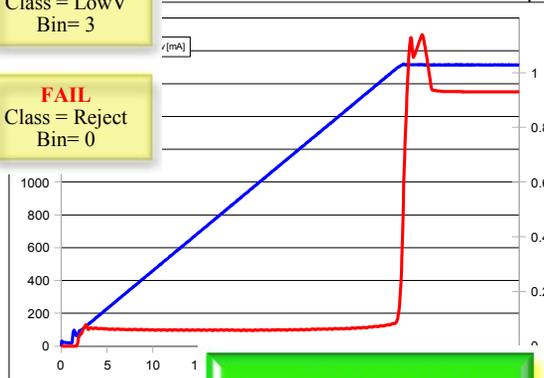
Simplified: for production use, it needs no particular operators training: just recall the proper testprogram

Complete: for testprogram definition and debugging, characterization, tests looping. It allows results and waveform sinspection.

Reports and statistics generation, database and spreadsheets connection.



IgI (IgI)	PASS	✓
Vigth (Vigth)	PASS	✓
VceOn (Vfd)	PASS	✓
L1 (IGBT 2.145V		✓
L2 (IGBT 2.140V		✓
IGBT 2.150V		✓
IGBT 2.135V		✓
IGBT 2.140V		✓
IGBT 2.155V		✓
PASS		✓
PASS		✓
PASS		✓
IGBT 1354V		✓
IGBT 1344V		✓
IGBT 1339V		✓
IGBT 1329V		✓
IGBT 1353V		✓
IGBT 1358V		✓



PASS